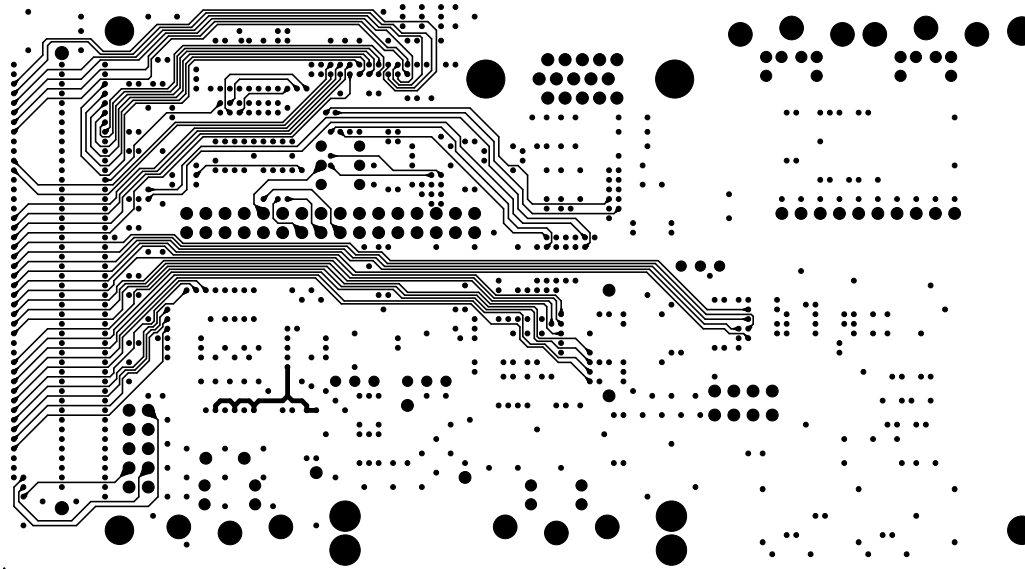


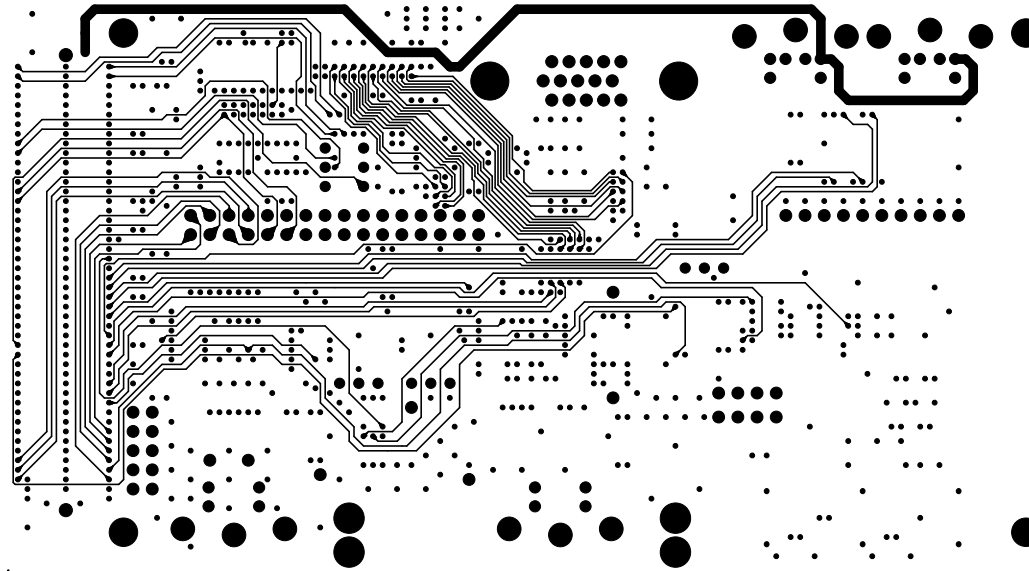
ADS	CUSTOMER: AVNET ELECTRONIC MARKETING	DATE: DEC-04-03
	TITLE: AUDIO/VISUAL CARD	REV: D
	LEVEL: PRIMARY COMPONENT SIDE - LAYER 1	



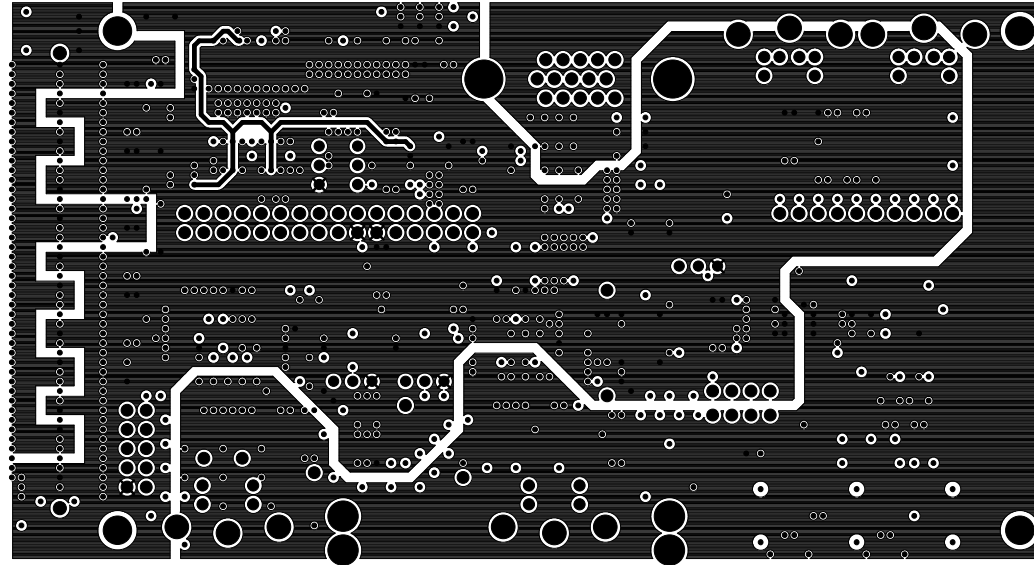
ADS	CUSTOMER: AVNET ELECTRONIC MARKETING	DATE: DEC-04-03
	TITLE: AUDIO/VISUAL CARD	REV: D
	LEVEL: GROUND PLANE - LAYER 2	



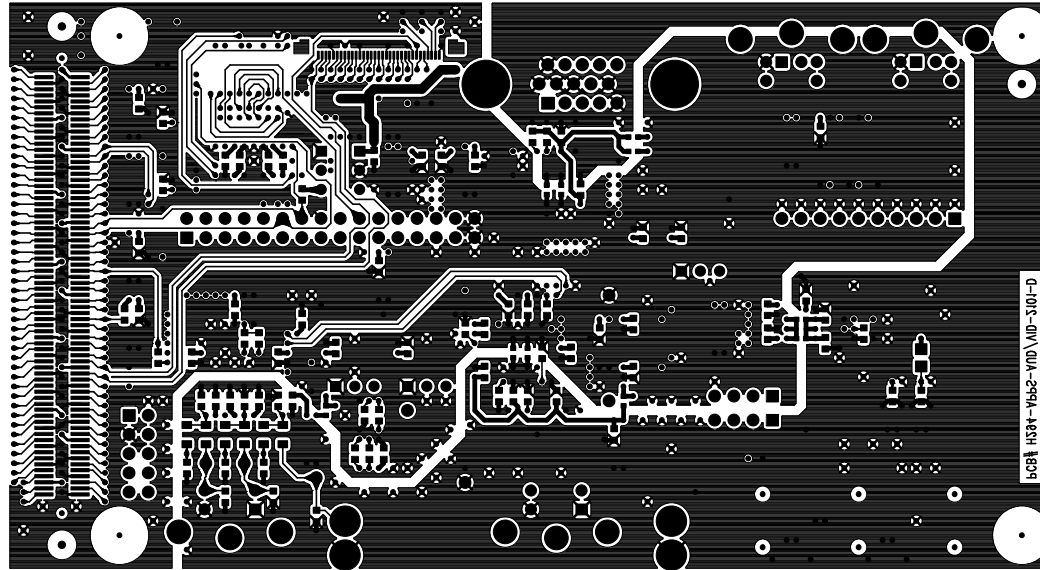
ADS	CUSTOMER: AVNET ELECTRONIC MARKETING	DATE: DEC-04-03
	TITLE: AUDIO/VISUAL CARD	REV: D
	LEVEL: INNER SIGNAL - LAYER 3	



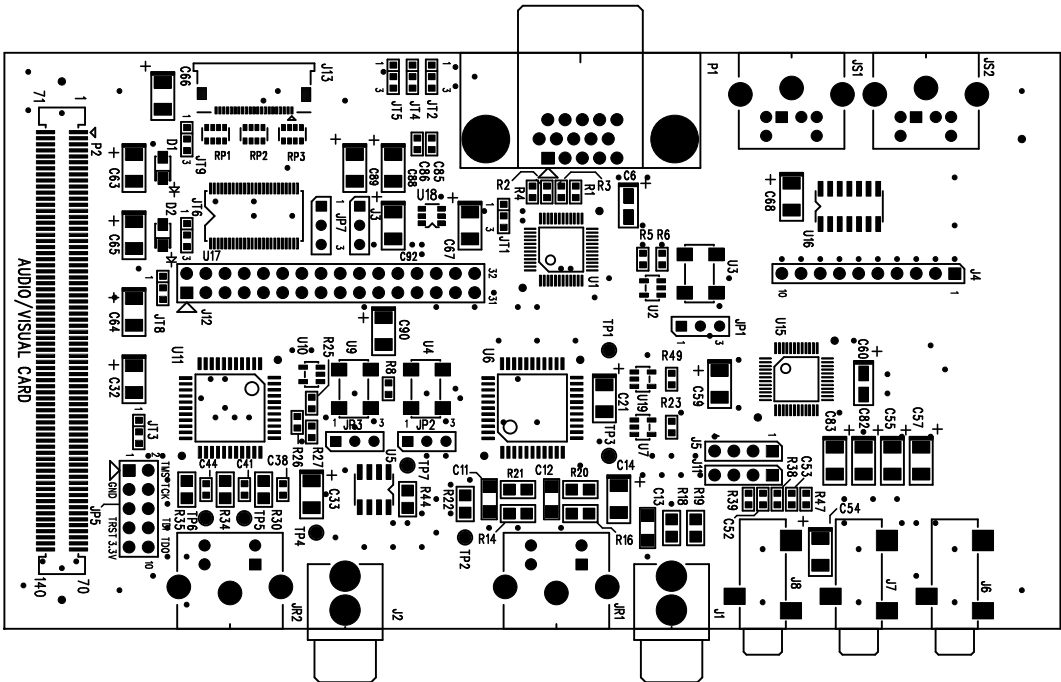
ADS	CUSTOMER: AVNET ELECTRONIC MARKETING	DATE: DEC-04-03
	TITLE: AUDIO/VISUAL CARD	REV: D
	LEVEL: INNER SIGNAL - LAYER 4	



ADS	CUSTOMER: AVNET ELECTRONIC MARKETING	DATE: DEC-04-03
	TITLE: AUDIO/VISUAL CARD	REV: D
	LEVEL: POWER PLANE - LAYER 5	

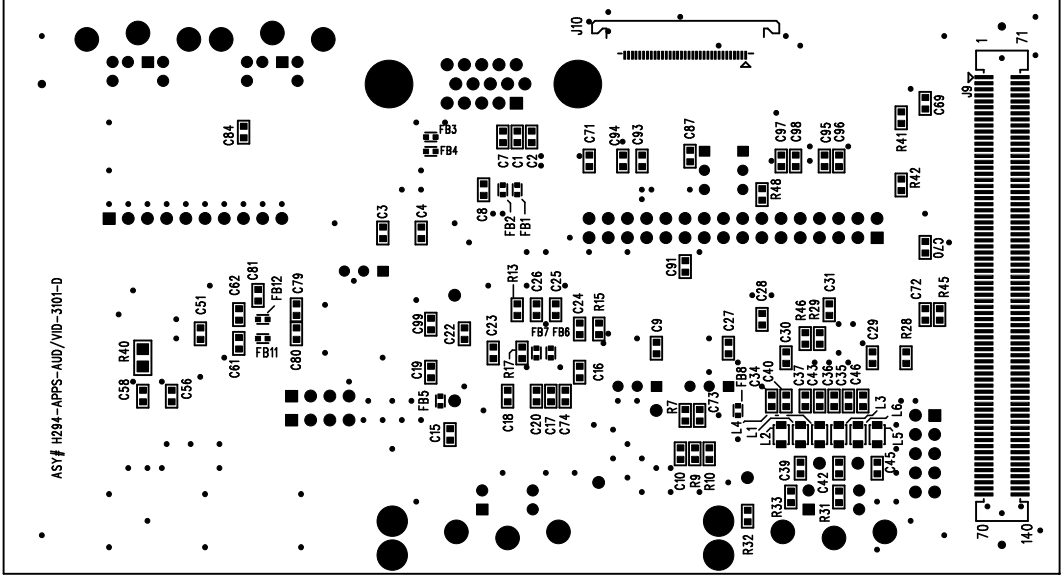


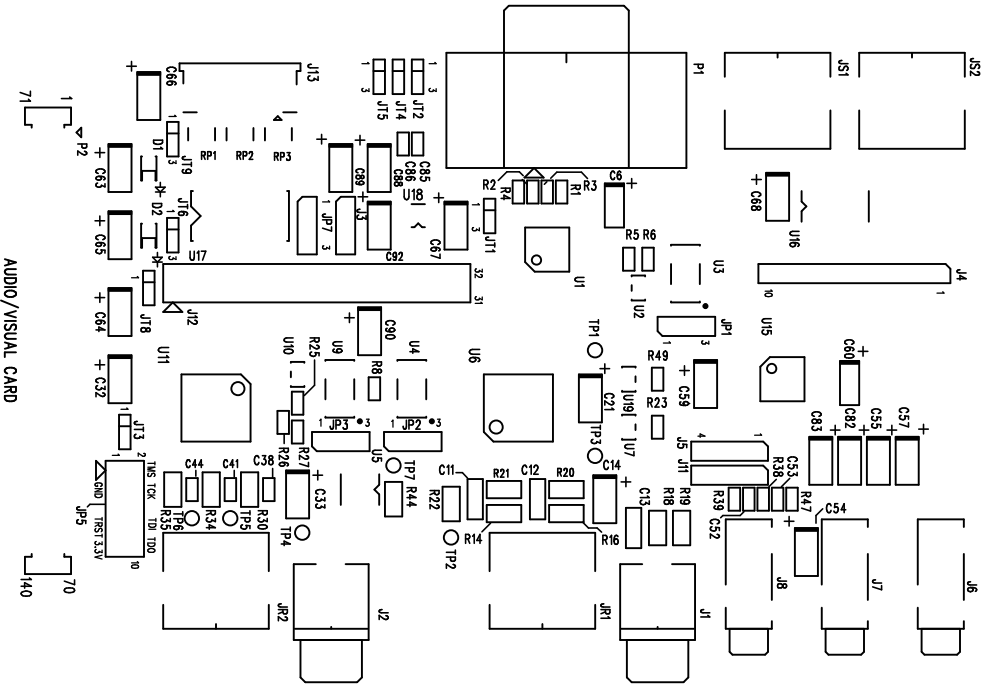
ADS	CUSTOMER: AVNET ELECTRONIC MARKETING	DATE: DEC-04-03
	TITLE: AUDIO/VISUAL CARD	REV: D
	LEVEL: SECONDARY COMPONENT SIDE - LAYER 6	



ADS	CUSTOMER: AVNET ELECTRONIC MARKETING		DATE: DEC-04-03
	TITLE: AUDIO/VISUAL CARD		REV: D
	LEVEL: PRIMARY SILKSCREEN		

LEVEL: SECONDARY SILKSCREEN	AD2
TITLE: AUDIO\AUDIO CARD	
REV: D	
CUSTOMER: AVNET ELECTRONIC MARKETING	
DATE: DEC-04-03	



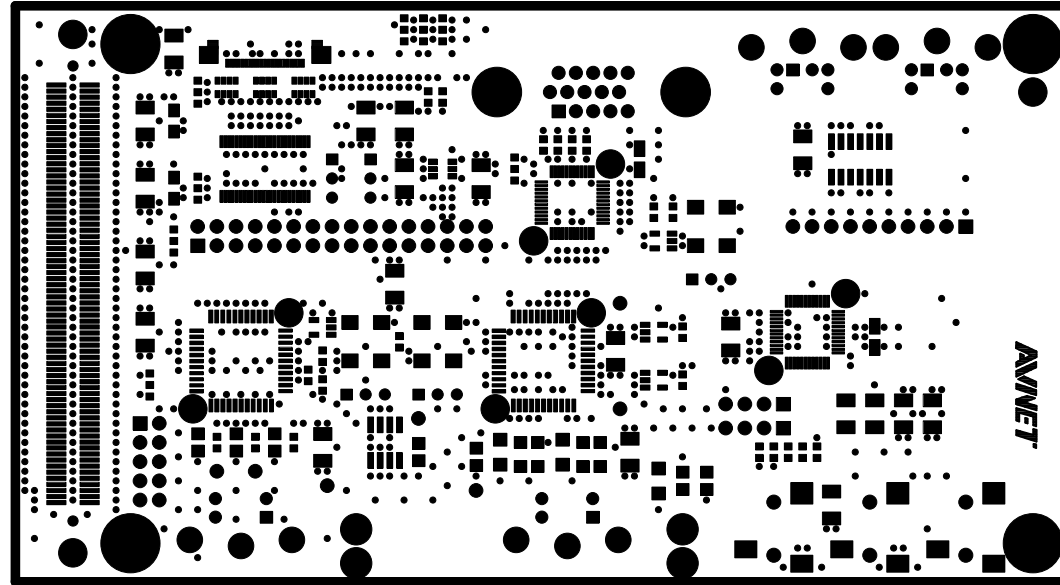


AUDIO/VISUAL CARD

ADS	CUSTOMER: AVNET ELECTRONIC MARKETING	DATE: DEC-04-03
	TITLE: AUDIO/VISUAL CARD	REV: D
	LEVEL: PRIMARY SILKSCREEN	

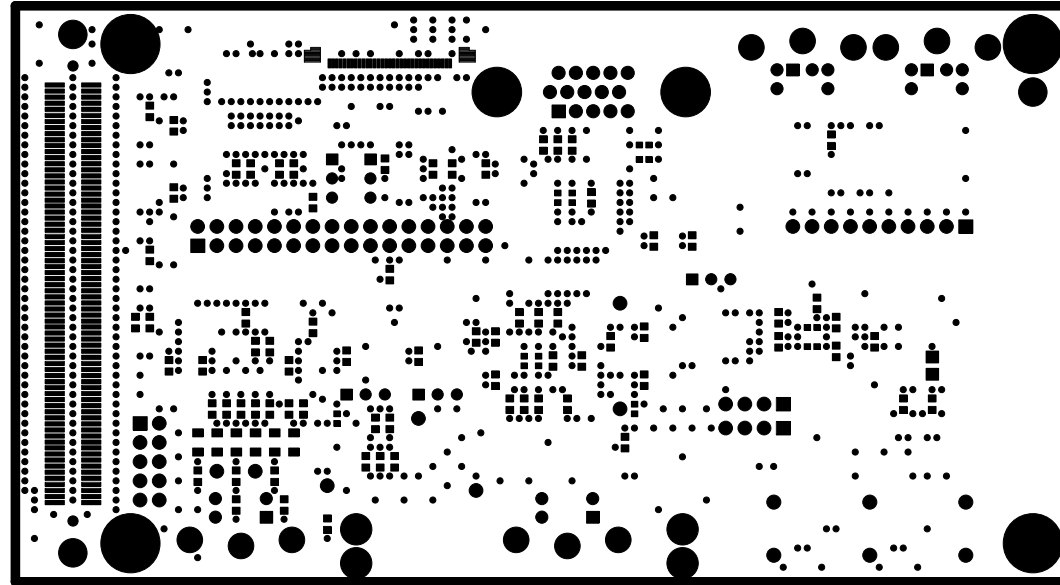


ADS	CUSTOMER: AVNET ELECTRONIC MARKETING	DATE: DEC-04-03
	TITLE: AUDIO/VISUAL CARD	REV: D
	LEVEL: SECONDARY SILKSCREEN	

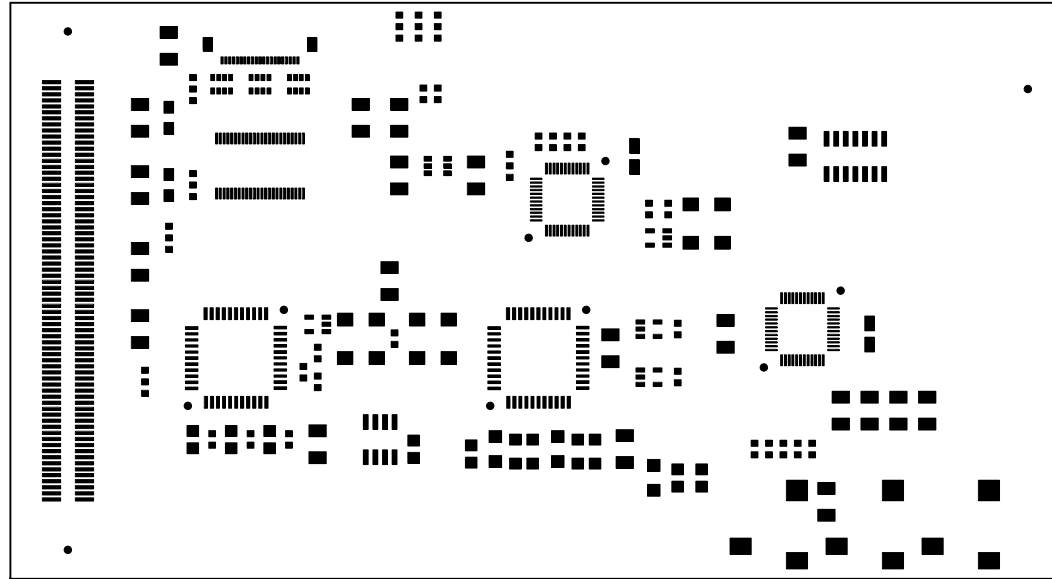


ADS	CUSTOMER: AVNET ELECTRONIC MARKETING	DATE: DEC-04-03
	TITLE: AUDIO/VISUAL CARD	REV: D
	LEVEL: PRIMARY SOLDER MASK	

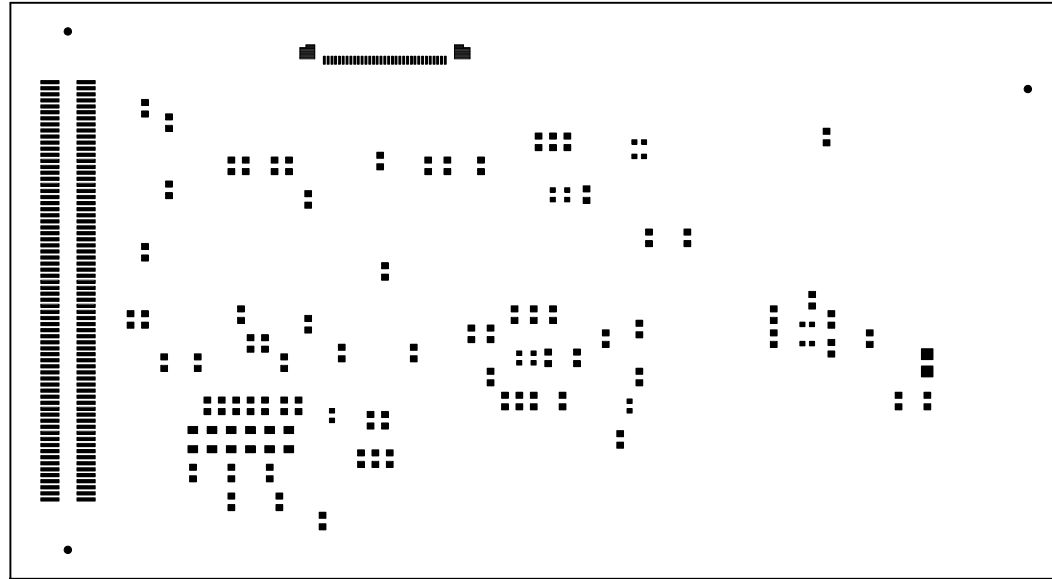




ADS	CUSTOMER: AVNET ELECTRONIC MARKETING	DATE: DEC-04-03
	TITLE: AUDIO/VISUAL CARD	REV: D
	LEVEL: SECONDARY SOLDER MASK	

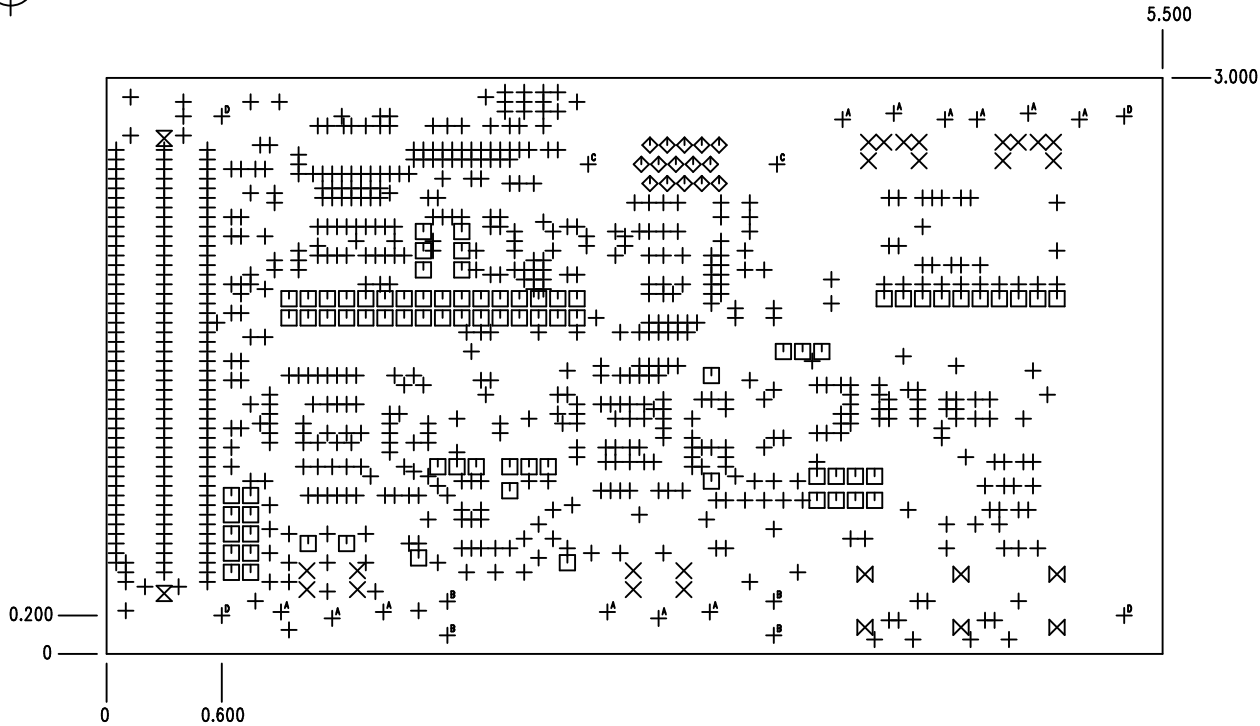


ADS	CUSTOMER: AVNET ELECTRONIC MARKETING		DATE: DEC-04-03
	TITLE: AUDIO/VISUAL CARD		REV: D
	LEVEL: PRIMARY SOLDER PASTE		



ADS	CUSTOMER: AVNET ELECTRONIC MARKETING		DATE: DEC-04-03
	TITLE: AUDIO/VISUAL CARD		REV: D
	LEVEL: SECONDARY SOLDER PASTE		

HOLE CHART			
DIA AFTER PLATING	QTY	SYM	PLATE THRU
.014 +.003 −.008	724	+	YES
.035 +/- .003	20	×	YES
.040 +/- .003	82	□	YES
.047 +/- .003	15	◇	YES
.047 +/- .003	2	⊗	NO
.067 +/- .003	6	⊠	NO
.090 +/- .003	12	+ ^A	YES
.110 +/- .005	4	+ ^B	YES
.125 +/- .005	2	+ ^C	YES
.142 +/- .005	4	+ ^D	NO



ADS	CUSTOMER: AVNET ELECTRONIC MARKETING	DATE: DEC-04-03
	TITLE: AUDIO/VISUAL CARD	REV: D
	LEVEL: FAB/DRILL IDENT	

NOTES: UNLESS OTHERWISE SPECIFIED

- BOARD SHALL BE FABRICATED AS PER IPC-A-600E GUIDELINES
- VENDOR LOGO, VENDOR P/N, REVISION AND DATE CODE OF MANUFACTURING SHALL BE ETCHED ON THE SOLDER SIDE.
THE DATE CODE SHALL BE IN THE FORMAT: "WWYY" WHERE
WW=WEEK AND YY = YEAR
- FABRICATE USING FILM "FAB/DRILL IDENT" FOR REFERENCE
- PERMANENTLY MARK BARE BOARD WITH TEST STAMP USING NON-CONDUCTIVE INK
- FINISHED BOARD SHALL MEET FLAMMABILITY RATING OF 94V-0 OR 94V-1 WHEN TESTED IN ACCORDANCE WITH UL STANDARD UL-94
- MATERIAL: FR-4 NATURAL EPOXY/FIBERGLASS WITH 0.5 OZ COPPER FOR EXTERNAL LAYERS AND 1 OZ COPPER FOR INTERNAL LAYERS
- SILKSCREEN BOTH SIDES WITH NON-CONDUCTIVE INK (COLOR = WHITE)
- SMOBC PER IPC-SM-840 TYPE B, CLASS III, LIQUID (COLOR = RED) MINIMUM THICKNESS 0.0003"
- FINISH SHALL BE HOT AIR SOLDER LEVEL (HASL) AFTER SMOBC
- ALL TEST POINTS SHALL BE FREE OF SOLDERMASK AND SILKSCREEN
- ALL HOLE SIZES ARE AFTER PLATING
- VENDOR MAY USE TEAR DROPS TO IMPROVE ANNULAR RINGS AS LONG AS DRC RULES ARE FOLLOWED
- FINISHED BOARDS SHALL NOT HAVE NICKS, SCRATCHES, VOIDS, EXPOSED COPPER, POOR PLATING OR MISDRILLED HOLES
- NO SILKSCREEN ALLOWED ON EXPOSED LANDS OR IN HOLES
- TIE-BARS ON THERMAL PADS SHOULD BE 15 MILS MINIMUM WIDTH
- MAKE ALL VIA CONNECTIONS TO POWER AND GROUND PLANES SOLID (IE: THERMALS WITH 25 MIL O.D. OR 45 MIL O.D. SHOULD BE REMOVED FROM INNER PLANE LAYERS)
- VENDOR MAY ADD COPPER THIEVING AS NEEDED TO IMPROVE MANUFACTURABILITY, THIEVING TO BE 0.030" ROUND PADS AT 0.050" SPACING. THIEVING WILL HAVE A MINIMUM OF 0.100" CLEARANCE FROM EXISTING COPPER AND SHOULD NOT BE PLACED UNDER SURFACE MOUNT DEVICES
- ALL UNCONNECTED PADS ON INNER SIGNAL LAYERS MUST BE REMOVED
- ALL FINISHED BOARDS TO BE 100% ELECTRICALLY TESTED
- UNLESS OTHERWISE INDICATED, ALL LINEAR TOLERANCES SHALL BE
XX +/- .010 AND XXX +/- .005

ADDITIONAL NOTES:

- FINISHED BOARD THICKNESS = 0.063"(+/- 0.007")
- ALL VIAS ON PRIMARY COMPONENT SIDE (LAYER 1) ARE TO BE TENTED WITH APPROPRIATE SOLDER MASK (SECONDARY OPERATION)

